



Attorney's Docket No. 03048.P008

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Christopher Songer, et al.

Application No.: 09/651,425

Filing Date: August 30, 2000

For: SYSTEM AND METHOD FOR PREPARING
SOFTWARE FOR EXECUTION IN A
DYNAMICALLY CONFIGURABLE HARDWARE
ENVIRONMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Examiner: Not Assigned

Art Unit: Not Assigned

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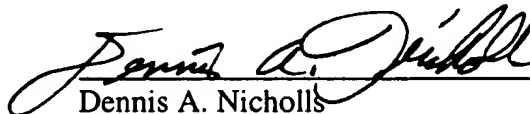
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Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 16 October 2000


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Substitute for form 1449A/PTO (Modified by BSTZ 6/30/99) INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known	
		Application Number	09/651,425
		Filing Date	August 30, 2000
		First Named Inventor	Christopher Songer
		Group Art Unit	Not Assigned
Examiner Name	Not Assigned		
Attorney Docket Number	03048.P008		
Sheet	1	of	2

U.S. PATENT DOCUMENTS					
Examiner Initials *		U.S.Patent Document	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Filing Date if Appropriate
		Number			
		6,122,719	Mirsky		
		5,915,123	Mirsky		
		6,108,760	Mirsky		
		5,742,180	DeHon et al.		
		4,967,340	Dawes		
		5,956,518	DeHon et al.		

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FOREIGN PATENT DOCUMENTS					
Examiner Initials *	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
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OTHER DOCUMENTS		
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	"Smart Compilers Puncture Code Bloat," Brown, Electronic Engineering Times, October 9, 1995 (pages 38 & 42).	
	"A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Razdan et al., Micro-27 Proceedings of the 27th Annual International Symposium on Microarchitecture, 11/30-12/2/94 (pp. 172-180).	
	"Programmable Active Memories: Reconfigurable Systems Come of Age," IEEE Transactions on VLSI Systems, 1995 (pp. 1-15).	
	"Pilkington Preps Reconfigurable Video DSP," Clark, EE Times, week of July 31, 1995.	
	"Coarse-Grain Reconfigurable Computing," Mirsky, Ethan A., Thesis submitted at the Massachusetts Institute of Technology, June 1996.	

Examiner Signature	Date Considered
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Examiner Name	Not Assigned
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Attorney Docket Number	03048.P008
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Sheet	2	of	2
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U.S. PATENT DOCUMENTS

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	"SOP: Adaptive Massively Parallel System," by Tsukasa Yamauchi et al., NEC Research & Development, Vol. 37, No. 3, July 1996 (pp. 382-393).	

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